21462844.80303 5652917.8857143 21178577.936709 38664262624 13156801.184615 18958575.767677 32628810.727273 10951851.068182 41797096495 20952935.5 7896089.3823529 14892536.387755 44679548.88 21260649.379747 14217499.1 116735689.7 5560885668 69555814225 69663451600 65689688196 461123428 38712359.6 29943916924 19295119854 2961740.971831

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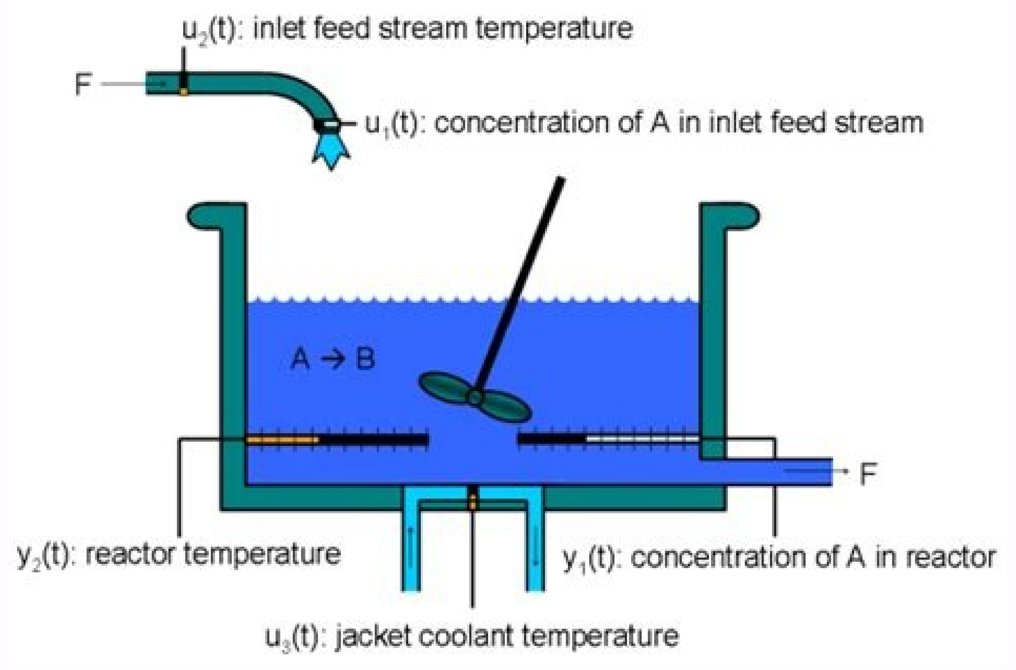
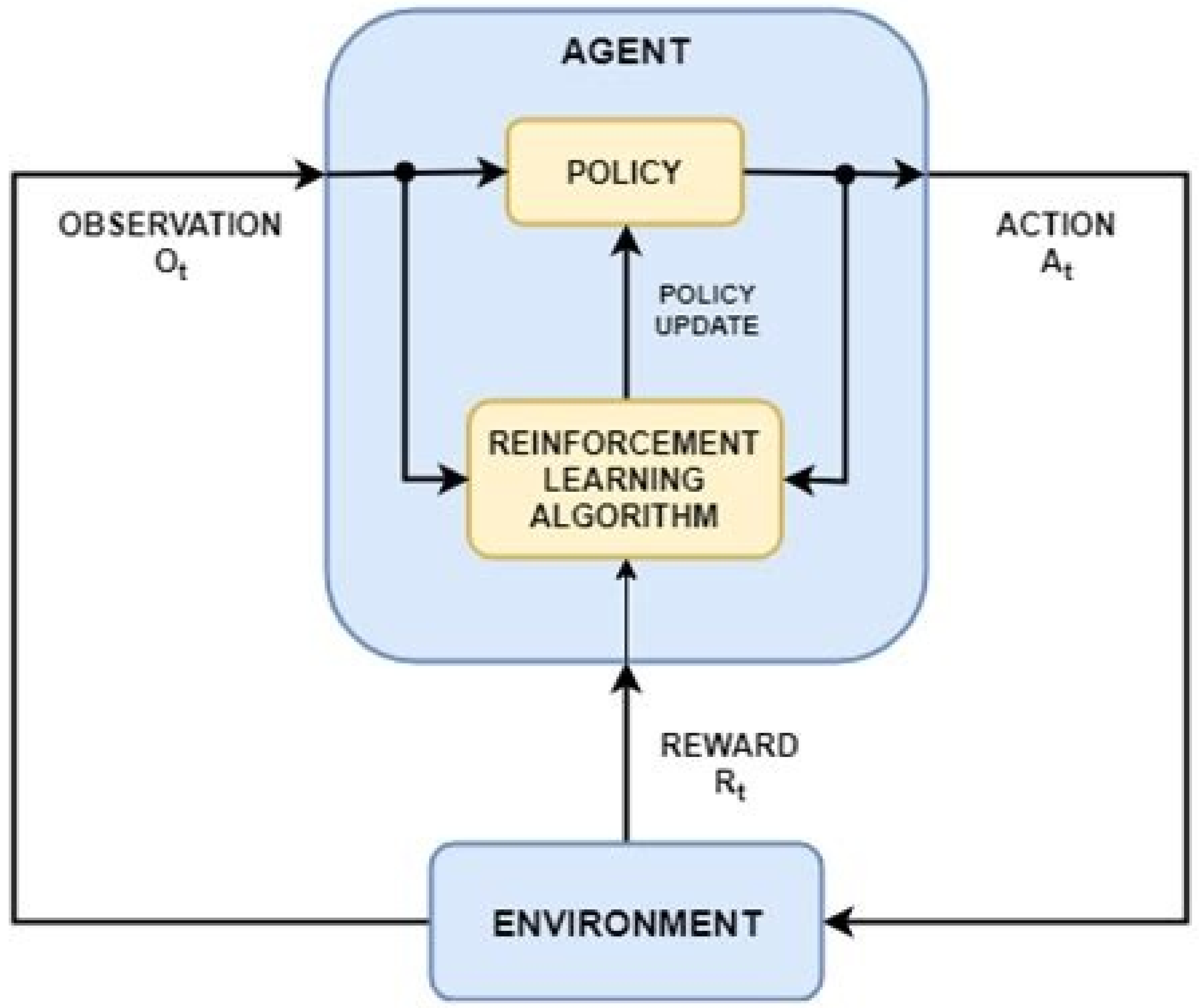
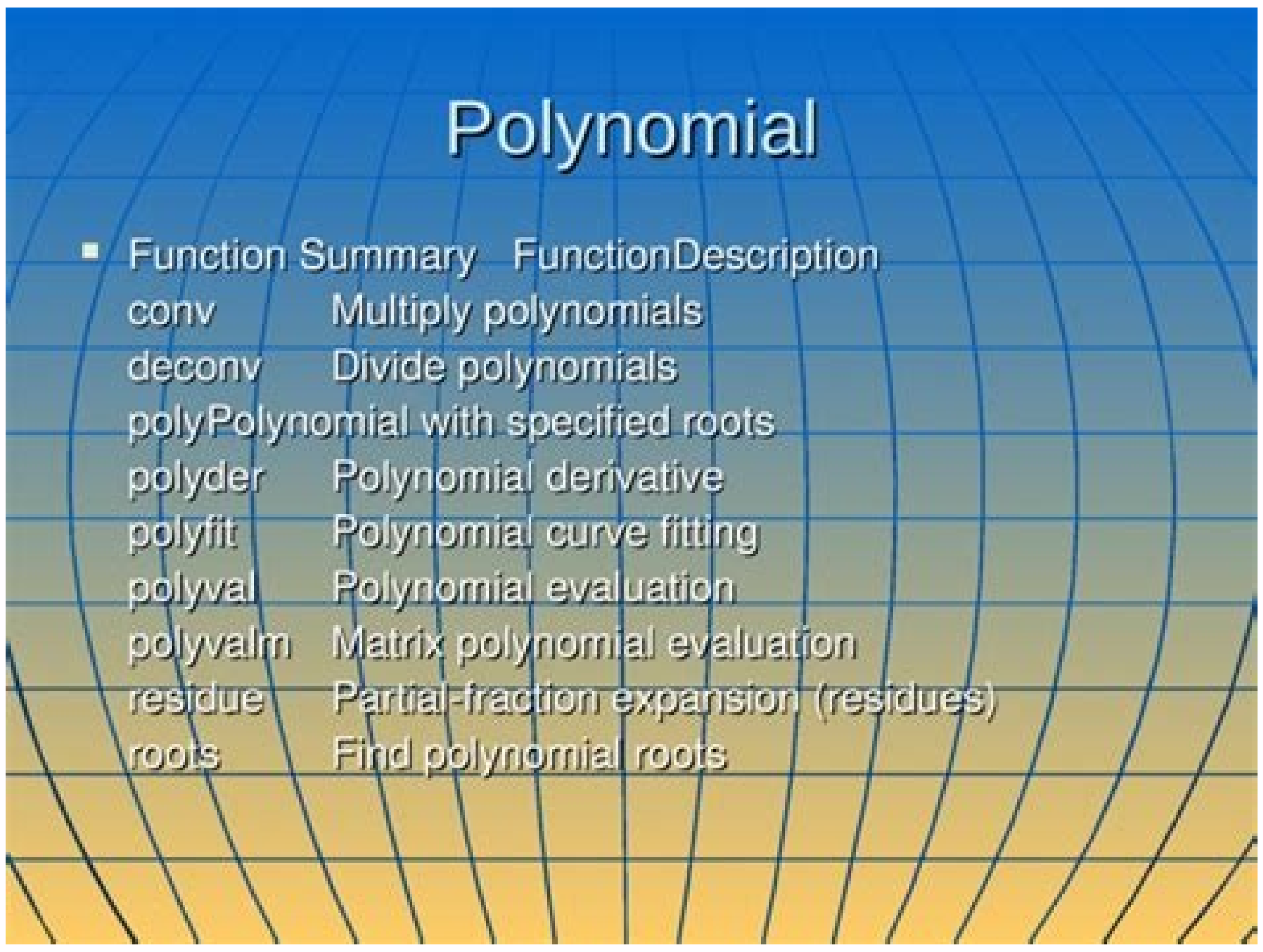
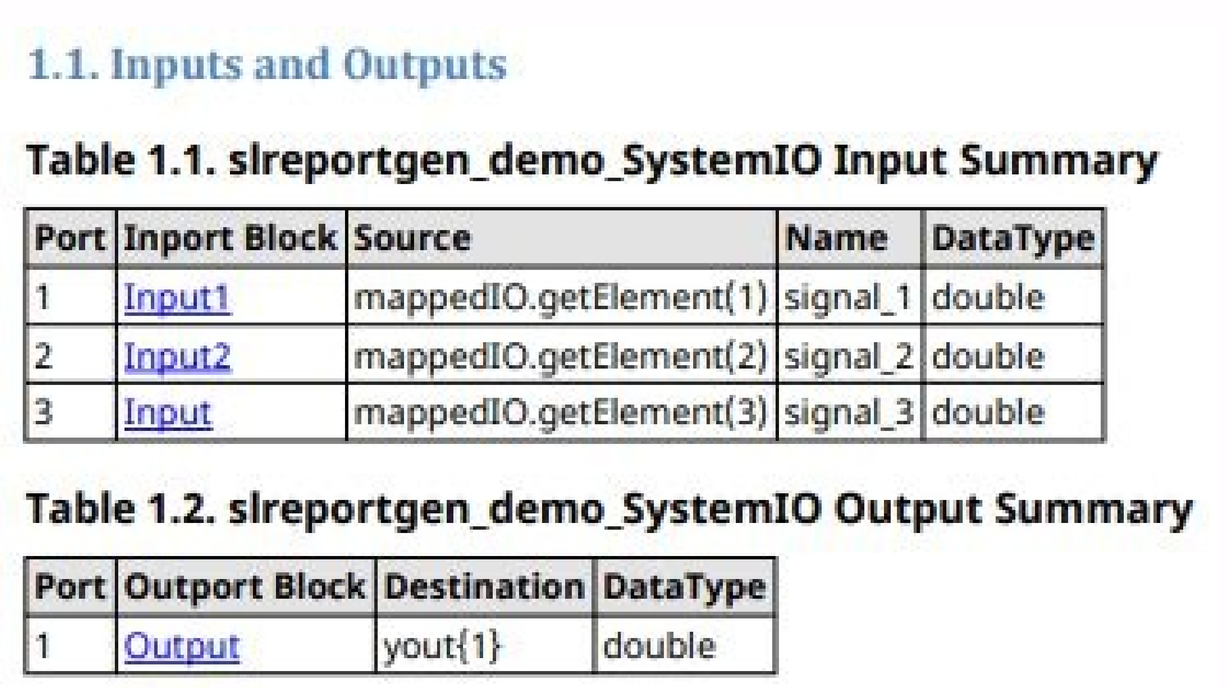








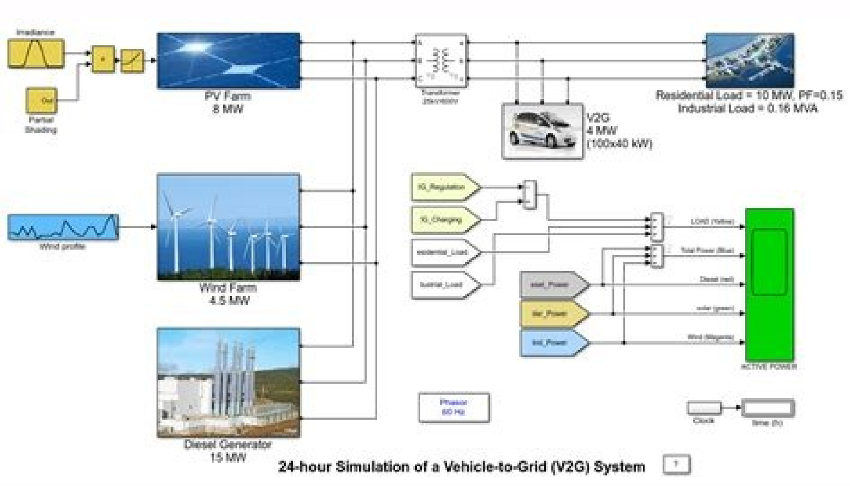
1.1. Inputs and Outputs

Table 1.1. slreportgen_demo_SystemIO Input Summary

Port	Inport Block	Source	Name	DataType
1	Input1	mappedIO.getElement(1)	signal_1	double
2	Input2	mappedIO.getElement(2)	signal_2	double
3	Input	mappedIO.getElement(3)	signal_3	double

Table 1.2. slreportgen_demo_SystemIO Output Summary

Port	Outport Block	Destination	DataType
1	Output	yout{1}	double



Simulink example. Simulink example models.

The pay transparency policy is available here. 1 of 1 models built (0 models already up to date) Build duration: 0h 0m 10.027s ### Preparing to start SIL simulation ... In a simulation, you run one Model block in SIL mode and the other Model block in normal mode.model='rtwdemo_sil_modelblock'; open_system(model); Turn off:Code coverageExecution time profilingcoverageSettings = get_param(model, 'CodeCoverageSettings'); coverageSettings.CoverageTool='None'; set_param(model, 'CodeCoverageSettings',coverageSettings); open_system('rtwdemo_sil_modelblock') set_param('rtwdemo_sil_modelblock', 'CodeExecutionProfiling','off'); open_system('rtwdemo_sil_counter') set_param('rtwdemo_sil_counter', 'CodeExecutionProfiling','off'); currentFolder=pwd; save_system('rtwdemo_sil_counter', fullfile(currentFolder,'rtwdemo_sil_counter.slx')) Configure state logging for the models.set_param('rtwdemo_sil_counter', 'SaveFormat','Dataset'); save_system('rtwdemo_sil_counter', fullfile(currentFolder,'rtwdemo_sil_counter.slx')) set_param(model, 'SaveFormat','Dataset'); set_param(model, 'SaveState','on'); set_param(model, 'StateSaveName', 'xout'); Test Top-Model CodeFor the Model block in SIL mode, specify generation of top-model code, which uses the standalone code interface.set_param([model '/CounterA'], 'CodeInterface', 'Top model'); Run a simulation of the test harness model.### Starting build procedure for: rtwdemo_sil_counter ### Successful completion of build procedure for: rtwdemo_sil_counter Build Summary Top model targets built: Model Action Rebuild Reason === rtwdemo_sil_counter Code generated and compiled Code generation information file does not exist. It's contributing to software products that make a difference in the world. The code from the top model uses the standalone code interface. See Choose a SIL or PIL Approach. MathWorks is privately held and has been profitable every year since its founding in 1984. Observe that the results match.fig1 = figure; subplot(3,1,1), plot(yout_normal), title('Counter Output for Normal Simulation') subplot(3,1,2), plot(yout_sil), title('Counter Output for SIL Simulation') subplot(3,1,3), plot(yout_normal-yout_sil), ... And it's being part of a company with an incredible commitment to doing the right thing—for each individual, our customers, and the local community. With this approach:You test code generated from subsystems, which uses the standalone code interface.You provide a test harness or a system model to supply test vector or stimulus inputs.You replace your original subsystem with the generated SIL or PIL block.Open a simple model, which consists of a control algorithm and a plant model in a closed loop. rtwdemo_sil_topmodel_data(T); Configure logging options in the model.set_param(model, 'LoadExternalInput','on'); set_param(model, 'ExternalInput','ticks_to_count, reset, counter_mode, count_enable'); set_param(model, 'SignalLogging', 'on'); set_param(model, 'SignalLoggingName', 'logsOut'); set_param(model, 'SaveOutput','on') Run a normal mode simulation.set_param(model,'SimulationMode','normal') sim_output = sim(model,10); yout_normal = [sim_output.yout.signals(1).values sim_output.yout.signals(2).values]; Run a top-model SIL simulation.set_param(model,'SimulationMode','Software-in-the-Loop (SIL)') sim_output = sim(model,10); yout_sil = [sim_output.yout.signals(1).values sim_output.yout.signals(2).values]; ### Starting build procedure for: rtwdemo_sil_topmodel ### Successful completion of build procedure for: rtwdemo_sil_topmodel Build Summary Top model targets built: Model Action Rebuild Reason === rtwdemo_sil_topmodel Code generated and compiled

Code generation information file does not exist. There are three ways of running SIL and PIL simulations. Engineers and scientists use simulation software for a variety of reasons: MathWorks has a hybrid work model that enables staff members to split their time between office and home. For details, see Embedded Coder Supported Hardware.SIL or PIL Simulation with a Top ModelTest generated model code by running a top-model SIL or PIL simulation. 1 of 1 models built (0 models already up to date) Build duration: 0h 0m 8.4968s ### Preparing to start SIL simulation ... title('Difference Between Normal and SIL'); Test Model Reference CodeFor the Model block in SIL mode, specify generation of referenced model code, which uses the model reference code interface.set_param([model '/CounterA'], 'CodeInterface', 'Model reference'); Run a simulation of the test harness model.### Starting serial model reference code generation build ### Starting build procedure for: rtwdemo_sil_counter ### Successful completion of build procedure for: rtwdemo_sil_counter Build Summary Code generation targets built: Model Action Rebuild Reason === rtwdemo_sil_counter Code generated and compiled rtwdemo_sil_counter.c does not exist. The hybrid model provides the advantage of having both in-person time with colleagues and flexible at-home life optimizations. For details, see Create PIL Target Connectivity Configuration for Simulink.For supported hardware, you can use target support packages. 'SilCounterBus','T','reset','ticks_to_count','Increment'}; save([model '_results'],simResults{:}); clear(simResults{:},'simResults') SIL or PIL Block SimulationTest generated subsystem code by using a SIL or PIL block in a simulation. 'out2','yout2','yout2_sil','yout2_normal', ... Test numerical equivalence between model components and production code that you generate from the components by using software-in-the-loop (SIL) and processor-in-the-loop (PIL) simulations. plot(xout2_normal), title('State Logging for Model Block SIL (Model Reference) Simulation') subplot(3,1,3), plot(xout2_normal-xout2_sil), ... • Develop new Simulink examples that demonstrate Simulink Core functionality • Expand Simulink Core workflows demonstrating collaborative workflows, data management, distributed simulation, agent-based modeling, co-simulation, and project management • Execute on implemention plans and develop a robust suite of unit and integration tests • Work with remote teams in an Agile development environment and apply lean and test-driven methodologies • Participate in and drive concept, design, and code reviews for functional changes • Provide continuous value to customers through regular enhancements, timely bug fixes, and refactor of legacy code Minimum Qualifications A bachelor's degree and 5 years of professional work experience (or a master's degree, or equivalent experience) is required. MathWorks develops MATLAB and Simulink, the leading technical computing software used by engineers and scientists. 1 of 1 models built (0 models already up to date) Build duration: 0h 0m 7.7615s ### Preparing to start SIL simulation ... Do you want to advance the state of Simulink as an Integration Platform? Do you like developing and demonstrating Model Based Design examples, workflows, and tools? The simulation software calculates the behavior of the model as conditions evolve over time or as events occur. Este webinar apresenta uma introdução ao Simulink, um ambiente para construção e simulação de sistemas dinâmicos multi-domínio, com o objetivo de construir projetos utilizando o conceito de Model-Based Design e produzir sistemas embarcados.As principais funcionalidades do Simulink serão apresentadas por meio da construção passo-a-passo de um sistema dinâmico genérico.Este webinar é recomendado para usuários que não tenham familiaridade com o Simulink® e que estejam interessados em aprender como utilizar essa ferramenta para melhorar a qualidade de seus projetos.A demonstração do software abordará:• Visão geral do ambiente SIMULINK®• O que é Simulink®?• Principais recursos do Simulink®• Demonstração – Getting started with Simulink®Product Focus:• Simulink® Recorded: 5 Aug 2009 Related Products Simulation software helps you predict the behavior of a system. You can use simulation software to evaluate a new design, diagnose problems with an existing design, and test a system under conditions that are hard to reproduce, such as a satellite in outer space. With a SIL simulation, you test source code on your development computer. Common representations for system models include block diagrams, schematics, and state diagrams. Using these representations you can model mechatronic systems, control software, signal processing algorithms, and communications systems. Its input and output ports match those of the control algorithm subsystem.close_system('untitled',0); slbuild([model '/Controller']) ### Starting build procedure for: Controller ### Successful completion of build procedure for: Controller ### Creating SIL block ... is an equal opportunity employer. MathWorks participates in E-Verify. These settings can differ from the hardware implementation settings that you use when building the model for your production hardware. The company employs more than 5,000 people in 16 countries, with headquarters in Natick, Massachusetts, USA. 'ticks_to_count','reset'}; save([model '_results'],simResults{:}); clear(simResults{:},'simResults') SIL or PIL Simulation with a Model BlockTest generated model code by using a test harness model that runs a Model block in SIL mode. For more information, see Configure Hardware Implementation Settings. Simulation software also includes visualization tools, such as data displays and 3D animation, to help monitor the simulation as it runs. For more information, see Code Interfaces for SIL and PIL.You use a test harness model or a system model to provide test vector or stimulus inputs.You can easily switch a Model block between the normal, SIL, and PIL simulation modes.Open an example model that has two Model blocks which reference the same model. Modeling is a way to create a virtual representation of a real-world system that includes software and hardware. In the working folder, you see that standalone code is generated for the referenced model unless generated code from a previous build exists.Compare the behavior of Model blocks in normal and SIL modes. The code from the referenced model uses the model reference code interface. With this approach:You can test code that is generated from a top model or a referenced model. plot(yout2_sil), title('Counter Output for Model Block SIL (Model Reference) Simulation') subplot(3,1,3), plot(yout2_normal-yout2_sil), ... You can expect the order of magnitude for differences between SIL and normal simulations to be close to the machine precision for single-precision data.Define an error tolerance for SIL simulation results that is based on the machine precision for the single-precision, normal simulation results.machine_precision = eps(single(yout_normal)); tolerance = 4 * machine_precision; Compare normal and SIL simulation results. ### Starting SIL simulation for component: rtwdemo_sil_counter ### Application stopped ### Stopping SIL simulation for component: rtwdemo_sil_counter The model block in SIL mode runs as a separate process on your computer. Iterating between modeling and simulation can improve the quality of the system design early, thereby reducing the number of errors found later in the design process. View The EEO is the Law poster and its supplement. Build Summary Top model targets built: Model Action Rebuild Reason === Controller Code generated and compiled Code generation information file does not exist. YOU + MathWorks = Unlimited Possibilities Contact us if you need reasonable accommodation because of a disability in order to apply for a position. title('Difference Between Normal and SIL'); Clean up.close_system(model,0); if ishandle(fig1), close(fig1), end clear fig1 simResults = {'yout_sil','yout_normal','model','T',... Before you can run PIL simulations, you must configure target connectivity. title('Difference Between Normal and SIL'); Clean up.close_system(model,0); if ishandle(fig1), close(fig1), end, clear fig1 simResults={'out','yout','yout_sil','yout_normal', ... Modeling and simulation are especially valuable for testing conditions that might be difficult to reproduce with hardware prototypes alone, especially in the early phase of the design process when hardware may not be available. Related Topics Learn More. Join our Simulink Core team and be responsible for designing and developing scalable software frameworks and services. The results match.yout = out.logsOut; yout_sil = yout.get('counterA').Values.Data; yout_normal = yout.get('counterB').Values.Data; fig1 = figure; subplot(3,1,1), plot(yout_normal), title('Counter Output for Normal Simulation') subplot(3,1,2), ... title('Difference Between Normal and SIL'); Compare the logged states of Model blocks from normal and SIL mode simulations.xout = out.xout; xout_sil = xout{1}.Values.Data; xout_normal = xout{2}.Values.Data; fig1 = figure; subplot(3,1,1), plot(xout_sil), title('State Logging for Normal Simulation') subplot(3,1,2), ... In the working folder, you see that model reference code is generated unless code from a previous build exists.Compare the behavior of Model blocks in normal and SIL modes. The results match.yout2 = out2.logsOut; yout2_sil = yout2.get('counterA').Values.Data; yout2_normal = yout2.get('counterB').Values.Data; fig1 = figure; subplot(3,1,1), plot(yout2_normal), title('Counter Output for Normal Simulation') subplot(3,1,2), ... In the third plot, the differences between the simulations lie well within the defined error tolerance.yout_sil = out.yout; tout = out.tout; fig1 = figure; subplot(3,1,1), plot(yout_normal), title('Controller Output for Normal Simulation') subplot(3,1,2), plot(yout_sil), title('Controller Output for SIL Simulation') subplot(3,1,3), plot(tout,abs(yout_normal-yout_sil),'g-', tout,tolerance,'r-'), ... With this approach:You test code generated from the top model, which uses the standalone code interface.You configure the model to load test vectors or stimulus inputs from the MATLAB workspace.You can easily switch the top model between the normal, SIL, and PIL simulation modes.Open a simple counter top model.model='rtwdemo_sil_topmodel'; close_system(model,0) open_system(model) To focus on numerical equivalence testing, turn off:Model coverageCode coverageExecution time profilingset_param(gcs, 'RecordCoverage','off'); coverageSettings = get_param(model, 'CodeCoverageSettings'); coverageSettings.CoverageTool='None'; set_param(model, 'CodeCoverageSettings',coverageSettings); set_param(model, 'CodeExecutionProfiling','off'); Configure the input stimulus data.[ticks_to_count, reset, counter_mode, count_enable] = ... ### Starting SIL simulation for component: rtwdemo_sil_topmodel ### Application stopped ### Stopping SIL simulation for component: rtwdemo_sil_topmodel Unless up-to-date code for this model exists, new code is generated and compiled. ### Updating code generation report with SIL files ... Explore examples that illustrate modeling and simulation of dynamic systems for specific industries and applications. 'Position', blockPosition); close_system('untitled',0); clear controllerBlock blockPosition Run the SIL simulation.### Preparing to start SIL block simulation: rtwdemo_sil_block/Controller(SIL) ... title('Normal and SIL Difference and Error Tolerance'); Clean up.close_system(model,0); if ishandle(fig1), close(fig1), end clear fig1 simResults={'out','yout_sil','yout_normal','tout','machine_precision'}; save([model '_results'],simResults{:}); clear(simResults{:},'simResults') Hardware Implementation SettingsWhen you run a SIL simulation, you must configure your hardware implementation settings (characteristics such as native word sizes) to allow compilation for your development computer. You can use the top model, Model blocks, or SIL and PIL blocks that you create from a subsystem. plot(yout_sil), title('Counter Output for Model Block SIL (Top-Model) Simulation') subplot(3,1,3), plot(yout_normal-yout_sil), ... Building with 'gcc'. Be part of a dynamic team that is responsible for the design, implementation, maintenance, and evolution of core Simulink components leveraging the next generation of computer aided engineering tools that run across multiple platforms and devices. To avoid the need to change hardware implementation settings between SIL and PIL simulations, enable portable word sizes. Are you are interested in applying your Simulink knowledge to solve complex modeling and simulation problems? MEX completed successfully. plot(xout_normal), title('State Logging for Model Block SIL (Top-Model) Simulation') subplot(3,1,3), plot(xout_normal-xout_sil), ... The MathWorks, Inc. To avoid losing your original subsystem, do not save your model in this state.controllerBlock = [model '/Controller']; blockPosition = get_param(controllerBlock,'Position'); delete_block(controllerBlock); add_block('untitled/Controller',[controllerBlock '(SIL)'],... If you answered yes to these questions, then we would like to speak with you. If the software components of this model are driven by mathematical relationships, you can simulate this virtual representation under a wide range of conditions to see how it behaves. To determine whether model components and generated code are numerically equivalent, compare SIL and PIL results against normal mode results. 1 of 1 models built (0 models already up to date) Build duration: 0h 0m 13.082s Alternatively, you can right-click the subsystem and select C/C++ Code > Build This Subsystem. The control algorithm regulates the output from the plant.model='rtwdemo_sil_block'; close_system(model,0) open_system(model) Run a normal mode simulationout = sim(model,10); yout_normal = out.yout; clear out Configure the build process to create the SIL block for testing.set_param(model,'CreateSILPILBlock','SIL'); To test the behavior on production hardware, specify a PIL block.To create the SIL block, generate code for the control algorithm subsystem. You see the SIL block at the end of the build process. With a PIL simulation, you test the compiled object code that you intend to deploy in production by running the object code on real target hardware or an instruction set simulator. The target connectivity configuration enables the PIL simulation to:Build the target application.Download, start, and stop the application on the target.Support communication between Simulink and the target.To produce a target connectivity configuration, you can use the supplied target connectivity API. In the dialog box that opens, click Build.To perform a SIL simulation of the controller and plant model in a closed loop, replace the original control algorithm with the new SIL block. The generated code runs as a separate process on your computer.Plot and compare the results of the normal and SIL simulations. To run a simulation, you need a mathematical model of your system, which can be expressed as a block diagram, schematic, state diagram, or even code. To learn more about modeling and simulation with block diagrams, see Simulink®. Responsibilities Collaborate in technical vision and design to demonstrate the use of core Simulink products. View the E-Verify posters here. We evaluate qualified applicants without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, disability, veteran status, and other protected characteristics. Additional Qualifications • Degree in Automotive, Aerospace, Mechanical, or Electrical Engineering • Collaborative Model Based Design and/or development workflow experience is a plus • Controls and physical modeling simulation is a plus • Automotive and/or Aerospace industry experience is a plus • Numerical methods and optimization techniques is a plus • Experience with MATLAB and Simulink is a plus It's the chance to collaborate with bright, passionate people. ### Starting SIL simulation for component: rtwdemo_sil_block ### Application stopped ### Stopping SIL simulation for component: rtwdemo_sil_block The control algorithm uses single-precision, floating-point arithmetic. title('Difference Between Normal and SIL'); Compare the logged states of Model blocks from normal and SIL mode simulations.xout2 = out.xout; xout2_sil = xout2{1}.Values.Data; xout2_normal = xout2{2}.Values.Data; fig1 = figure; subplot(3,1,1), plot(xout2_sil), title('State Logging for Normal Simulation') subplot(3,1,2), ...

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